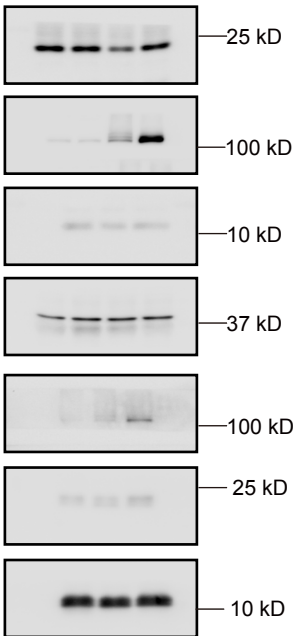


Fig. 2A

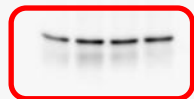




E



ITCH input



GAPDH



ITCH ip



M